

<u>PATENT</u>

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Chow, Lap-Wai; et al.	Examine	r: Lee, Eugene
Serial No.:	10/735,841	Art Unit: 2815	
Filed:	December 12, 2003	Our Ref:	B-4425NP 621267-1
For: "Integrated circuit modification using ) well implants" )		Date:	April 18, 2005
	, , , , , , , , , , , , , , , , , , ,	Re: Election/	Response to Restriction Requirement

## **RESPONSE**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is in reply to the Election/Restriction Requirement mailed on March 23, 2004, an initial response to which is due no later than

## April 23, 2005.

Please consider the following remarks. All remarks herein are made without prejudice.

Remarks/Arguments begin on page 2 of this paper.